

MARTINTONI DAVIDE

PERSONAL INFORMATION

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Nation: Italy

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Age: 33

Github page: <https://github.com/rakwaht>

EXPERIENCE

Collins Aerospace

Jan 2021 Now

Senior Cyber Security Engineer

- Work on Security-by-design, security risk assessment methods and tools, dynamic architecture, modern network security and DevSecOps in the context of avionics and modern manufacturing.
- Analyzing and reporting on cutting edge methodologies for security in a modern industrial context.
- Measuring and improving adherence to civil aviation US/EU cyber-security regulations (DO-326, DO-355/6, ...) and to industrial security standard such as NIST SP 800-171, IEC 62443-3-3 and ISO27001
- Exploring secure collaborative manufacturing for Industry 4.0 (COLLABS H2020 project)

Raytheon Technologies - RTX

Apr 2020 Dec 2020

Cyber Security Engineer

- Working in the cyber security discipline inside the formal method group. As central entity we work closely with the RTX business units to push cutting edge security methodologies in the manufacturing process and the final products. On top of that we participate in EU funded projects H2020 to further expand our knowledge and create research partnership with other companies and institutions. In particular I am involved on projects on:
 - Model-based Risk assessment
 - Threat modeling
 - Security-by-design
 - Distributed technologies & blockchain industrial application

I am also a proud member of the H2020 COLLABS consortium member exploring secure collaborative manufacturing.

Advanced Laboratory of Embedded Systems - ALES - UTRC

Feb 2017 Apr 2020

Research Engineer in Cyber Security

- Working in the cyber security group, applying model-based approach and formal methods we develop methodologies targeting to the "security by design" principle required in the product-development life cycle of UTC products. Main skills developed:
 - Security architecture design on industrial cyber physical systems
 - Methodology development for architecture-based risk assessment, implementing a Python prototype that use SMT techniques to exploit formal models in order to automatically extract threats to the system
 - Automated vulnerability assessment for third party components included in complex systems

SAP Labs France SAS

Mar 2016 Sept 2016

Researcher Intern in Cyber Security

- Part of security research team at SAP Labs France, improving and implementing new features for a project that aim to assess the impact of the vulnerability present in the open source libraries that everyone may use in it's own software. With this analysis every developer can know if a vulnerability should be patched as soon as possible updating the library even in its production environment or if the bug is present in a portion of the library that cannot affect its software and so he can simply wait for its next programmed patch.

Belka srl and Tweppy

Jan 2014 Jan 2016

Full stack Developer

- Worked as full stack developer for a couple of companys while studyng for my graduation. I mainly worked designing and implementing systems with ReactJS, C for HW interaction and C# for on Azure.

EDUCATION

Master of Science in Computer Science, University of Trento

Sept 2014 - Oct 2016

I attended a program that focus on cybersecurity and data analysis as main targets. I graduated with 110/110 cum laude with a final thesis entitled "Dynamic analysis for vulnerabilities in OSS software" based on my work done in a 6 months internship at SAP security research lab France.

TECHNICAL STRENGTHS

Core competencies	security by design, model-based risk analysis, newtork security, SDN model based design, formal verification, pentesting, blockchain, firewall IAM systems, log management
Computer Languages	C/C++, Assembly, Python, Ruby, Java, MATLAB, Javascript(DevExtreme and ReactJS)
Tools	Kali Linux, Metasploit, nmap, Ettercap, Vulas, gdb
Languages	Italian native tongue, English fluent

REFERENCES

Senior Security Researcher & Co-founder of V-Research Marco Rocchetto - marco@v-research.it.com

Senior Security Researcher Henrik Plate - henrik.plate@sap.com